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2816 DATE MAILED: 12/16/2008

# NOTICE OF ALLOWANCE AND FEE(S) DUE

20277 7590 12/16/2008 MCDERMOTT WILL & EMERY LLP 600 13TH STREET, N.W. WASHINGTON. DC 20005-3096 EXAMINER
HILTUNEN, THOMAS J
ART UNIT PAPER NUMBER

 APPLICATION NO.
 FILING DATE
 FIRST NAMED INVENTOR
 ATTORNEY DOCKET NO.
 CONFERMATION NO.

 10/511,165
 10/14/2004
 Shiro Sakiyama
 71971-015
 6689

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT WITH REDUCED SPEED VARIATIONS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(8) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/16/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 1SI. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and I/2 the ISSUE FFE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

#### PART B - FEE(S) TRANSMITTAL

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INSTRUCTIONS: This appropriate. All further indicated unless correcte maintenance fee notificat	form should be used for correspondence including d below or directed oth ions.	for transmitting the II ng the Patent, advance nerwise in Block 1, b	SSUE FEE and PUBLICA e orders and notification or y (a) specifying a new cor				
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							(Signature)
							(Date)
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EXAM	INER	ART UNIT	CLASS-SUBCLASS				
HILTUNEN,	THOMAS J	2816	327-534000				
"Fee Address" indi PTO/SB/47; Rev 03-0 Number is required.  3. ASSIGNEE NAME A	ess an assignee is ident 1 in 37 CFR 3.II. Comp	" Indication form aed. Use of a Customo		ntively,  ngle firm (having as a  r agent) and the nam  ttorneys or agents. If  be printed.  type)  patent. If an assign  assignment.	a memb nes of u no nan	er a 2p to p to se is 3	ocument has been filed for
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NOTE: The Issue Fee and interest as shown by the r	I Publication Fee (if req ecords of the United Sta	uired) will not be acce ites Patent and Traden	epted from anyone other that nark Office.	n the applicant; a reg	istered :	attorney or agent; or th	e assignee or other party ir
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PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010. OMB 0651-0033



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600 13TH STREET, N.W.			ART UNIT	PAPER NUMBER	
WASHINGTON, DC 20005-3096			2816		

# Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

# Application No. Applicant(s) 10/511.165 SAKIYAMA ET AL. Notice of Allowability Examiner Art Unit Thomas J. Hiltunen 2816 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. 1. This communication is responsive to amendment filed 26 September 2008. The allowed claim(s) is/are 10,12 and 15-17. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). b) ☐ Some\* c) ☐ None of the: a) 🔯 All 1. A Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). \* Certified copies not received: \_\_\_\_\_. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s)

U.S. Patent and	Trademark Offic
PTOI -37 (R	ev 08-06)

1. | Notice of References Cited (PTO-892)

Paper No./Mail Date

of Biological Material

2. Notice of Draftperson's Patent Drawing Review (PTO-948)

4. T Examiner's Comment Regarding Requirement for Deposit

Information Disclosure Statements (PTO/SB/08).

5. Notice of Informal Patent Application

 Interview Summary (PTO-413), Paper No./Mail Date .

9. ☐ Other .

7. X Examiner's Amendment/Comment

8. X Examiner's Statement of Reasons for Allowance

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#### DETAILED ACTION

#### Examiner's Amendment

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Eric Shelton Reg. No. 57,630 on 12/11/08.

### Amendment To The Claims

With respect to claim 15, the recitation of "The" on line 1 is to be changed to -- A --. The recitation of "of claim 13" on line 1 is to be deleted. The recitation of -- comprising:

a main circuit including a MOS transistor, and operating while receiving an operating power supply voltage; and

a power supply voltage control circuit for controlling the operating power supply voltage supplied to the main circuit, wherein:

a target saturation current value of the MOS transistors is set in the power supply voltage control circuit;

the power supply voltage control circuit controls a voltage value of the operating power supply voltage supplied to the main circuit so that an actual saturation current Application/Control Number: 10/511,165

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value of the MOS transistors of the main circuit is equal to the target saturation current value; and -- is to be inserted between lines one and two.

With respect to claim 16, the recitation of "The" on line 1 is to be changed to -- A --. The recitation of "of claim 13" on line 1 is to be deleted. The recitation of -- comprising:

a main circuit including a MOS transistor, and operating while receiving an operating power supply voltage; and

a power supply voltage control circuit for controlling the operating power supply voltage supplied to the main circuit, wherein:

a target saturation current value of the MOS transistors is set in the power supply voltage control circuit;

the power supply voltage control circuit controls a voltage value of the operating power supply voltage supplied to the main circuit so that an actual saturation current value of the MOS transistors of the main circuit is equal to the target saturation current value; and -- is to be inserted between lines one and two.

### Reasons For Amendment

Claims 15 and 16 have been amended to insert all the limitations of the previously allowed claims filed 04/30/08 into the currently pending claims filed 9/26/08.

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# Allowable Subject Matter

With respect to claim 10, there is no cited art that discloses, the constant current source generating a plurality of constant current values and selectively outputting one of the constant current values in the circuit as recited in claim 10. So et al. (USPN 5,883,544) discloses only outputting one constant current value IDS'.

With respect to claim 12, there is no cited art that discloses, the constant current generation circuit including an adjustment circuit for reducing variations in the generated constant current value in combination with the circuit as recited in claim 12. So et al. fails to disclose such an "adjustment circuit", nor is there any seen motivation for adding such a circuit.

With respect to claim 15, clearly it can be seen that Kaenel et al. (USPN 5,682,118) does not disclose "the target saturation current value of the MOS transistors of the main circuit is in a linear function relationship with the operating power supply voltage supplied to the main circuit". In fact, Fig. 10 discloses a logarithmic relationship between the current and supply voltage. Thus Kaenel et al. teaches away from a linear relationship between the saturation current and the supply voltage. Furthermore, there is no disclosure or motivation provided to have a linear relationship between the supply voltage and saturation current. It can be seen that Tang et al. (USPAPN 20040070440) discloses in Fig. 5 a linearly relationship between supply voltage bias voltage, and Vout'. Thus, the saturation current and supply voltage would have an inherent linear relationship. However, Tang fails to disclose the specific "power supply voltage control

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circuit" of as recited in claim 13. Thus, Tang fails to disclose all the recited limitations of claim 15. Therefore, claim 15 is allowable.

With respect to claim 16, it can be seen that claim 16 recites the same linear relationship between the target saturation current value and the operating power supply voltage value of claim 15. Thus, claim 16 is allowable for at least the same reasons as claim 15.

# Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas J. Hiltunen whose telephone number is (571)272-5525. The examiner can normally be reached on M-F 8:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lincoln Donovan can be reached on (571)272-1988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

TH
December 12, 2008
//Donovan Lincoln/
Supervisory Patent Examiner, Art Unit 2816